

# On-chip Network Topology

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## **Introduction**

In computer programming and systems architecture, topology refers to the formal geometric or logical arrangement of interconnected elements within a system. Depending on the abstraction layer, it typically describes how data flows between nodes or how components are physically/logically mapped. The choice of topology of a network affect.

## **Nomenclature**

The following chapter introduces the reader to various nomenclature used in the world of on-chip networks

## **Channels and Nodes**